

FIG. 1

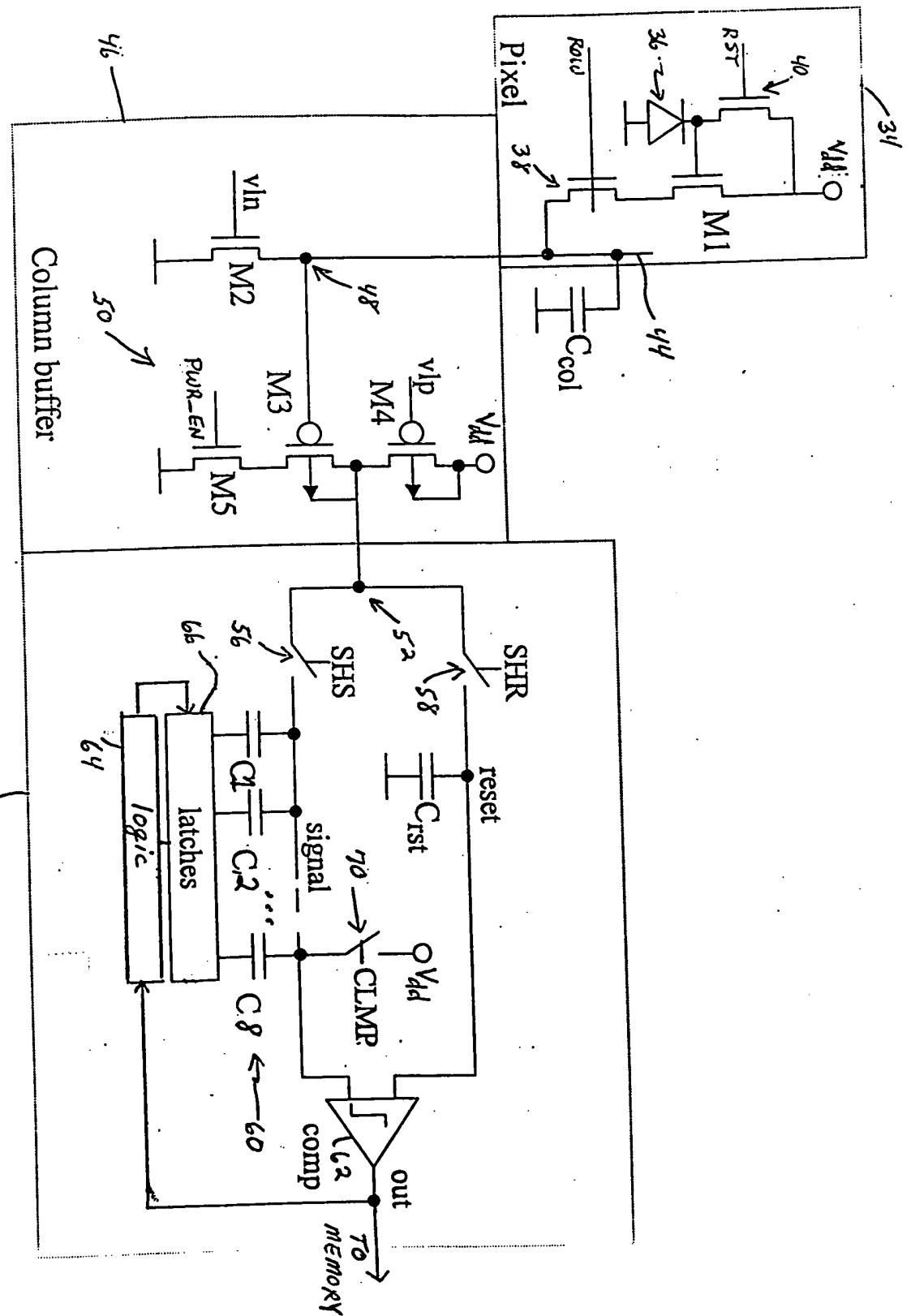


FIG. 2

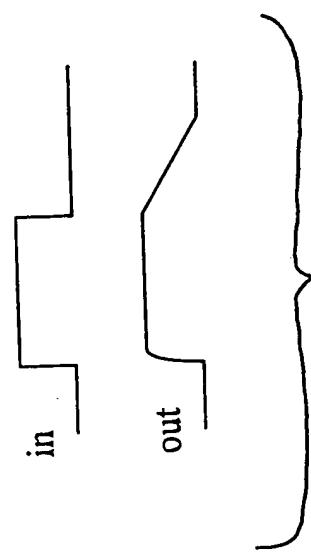
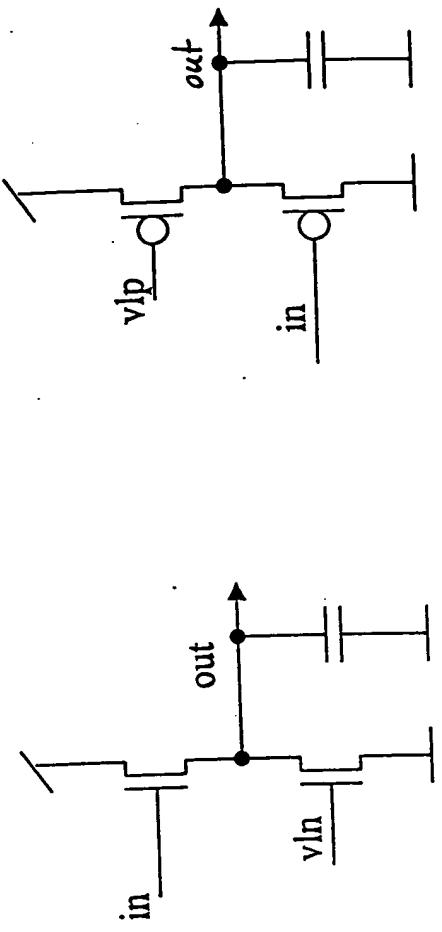


FIG. 3A

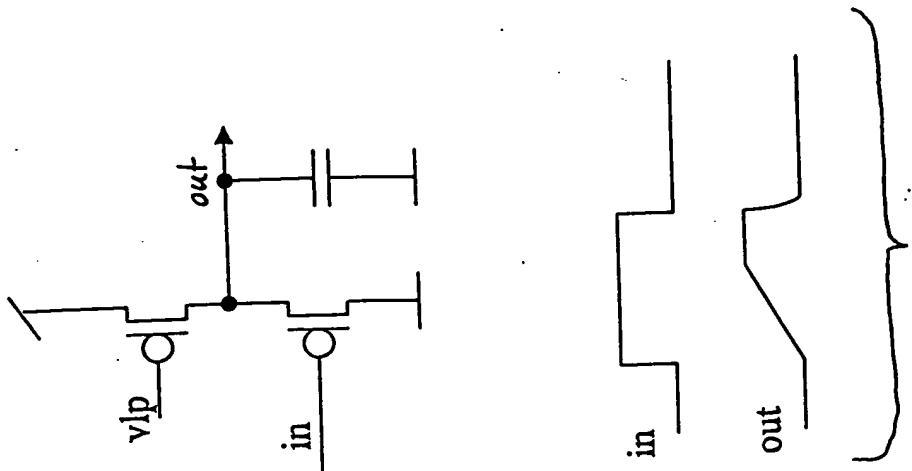


FIG. 3B

Row and PWR-EN

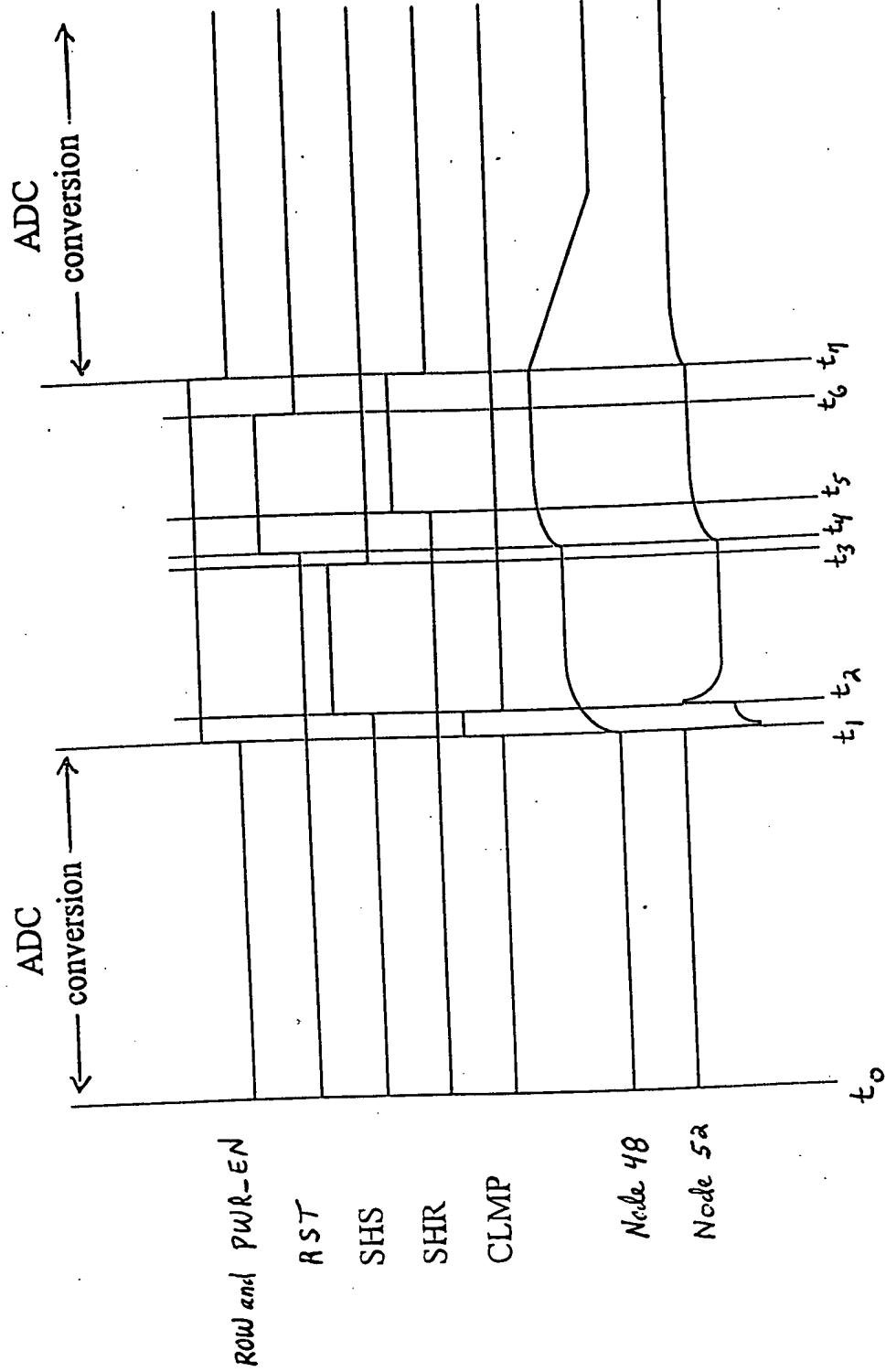


FIG. 4

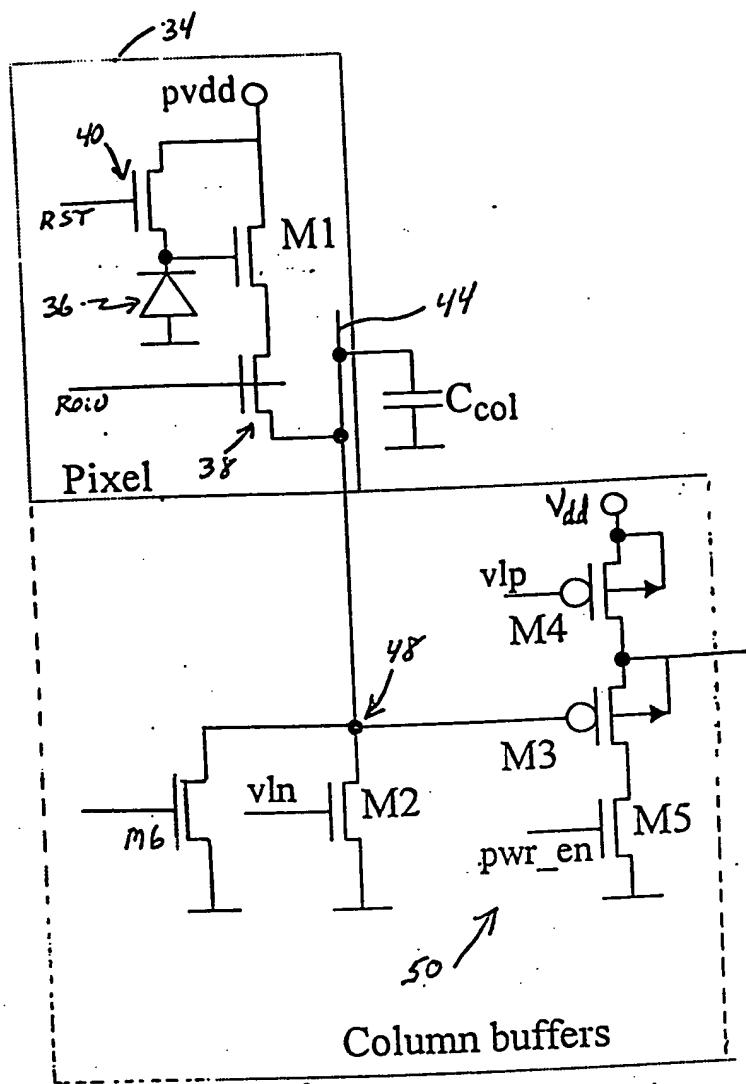


FIG. 5

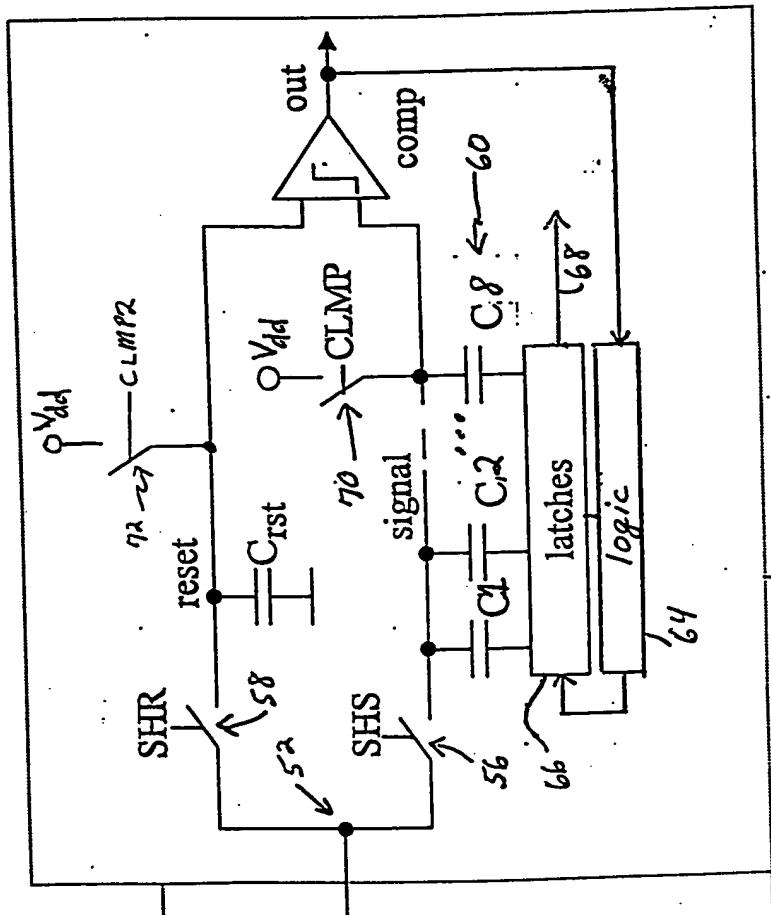
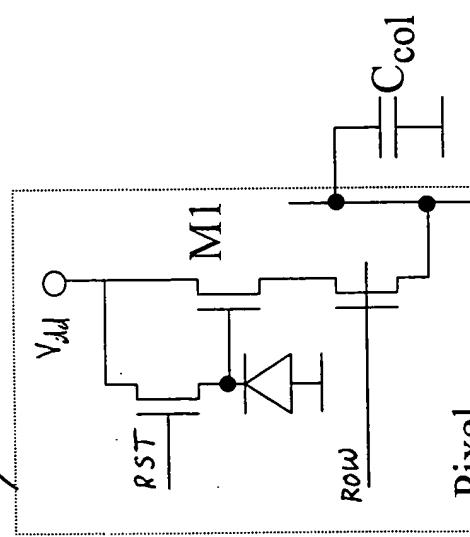


FIG. 6.

34



Pixel

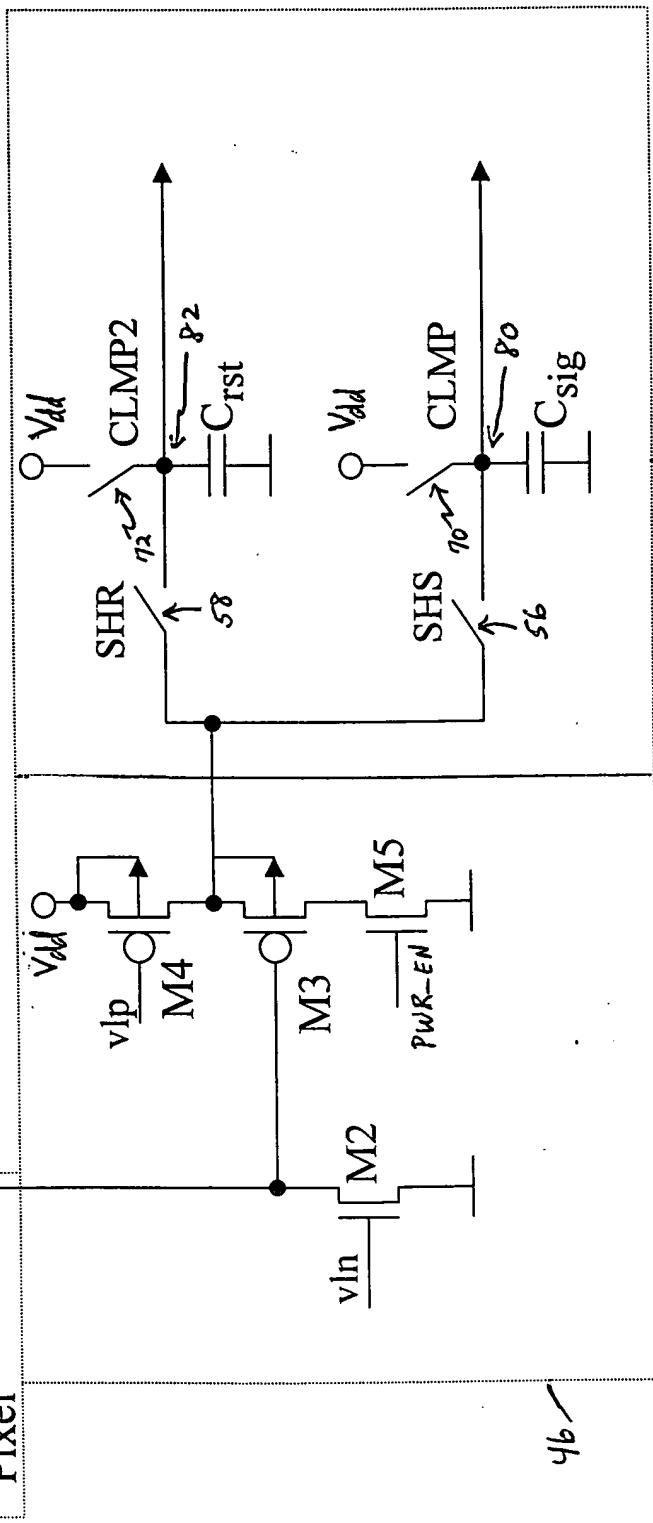


Fig. 7

54A

34

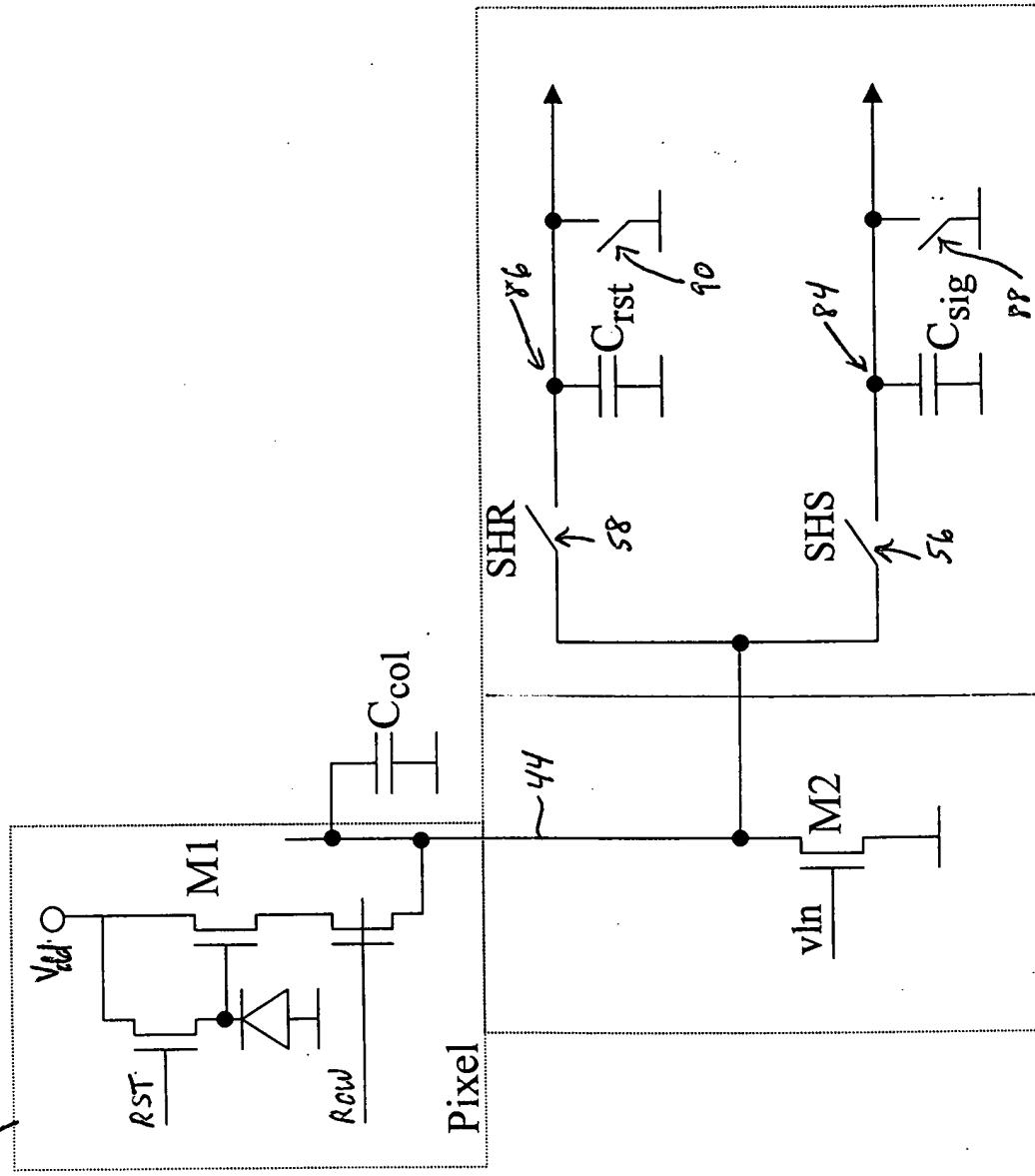
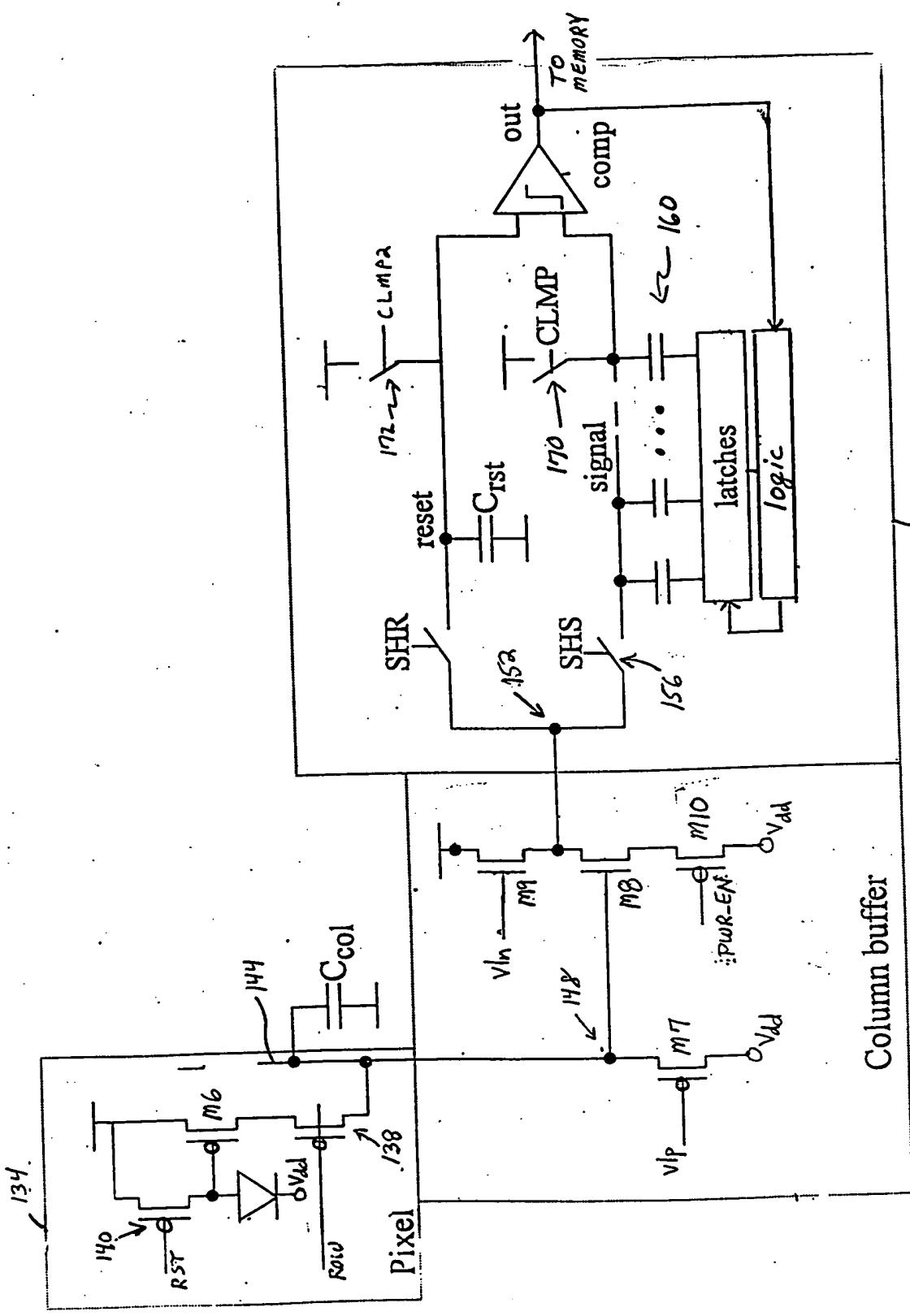


FIG. 8



Column buffer

154

FIG. 9